Class	Subclass	ISSUE CLASSIFICATION
438	41	

PATENT NUMBER

1080 U.S. PTO
09/987884
11/16/01

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
MT SCANNED QA	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/987884	F	~~257~~ 438	257	~~2815~~ 2814	

APPLICANTS

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TITLE

Semiconductor integrated circuit device and manufacturing method thereof

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)	
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)
438	41	438	~~257~~ 187 228

INTERNATIONAL CLASSIFICATION

H01L 21/283

☐ Continued on Issue Slip Inside File Jacket

☐ TERMINAL DISCLAIMER

☐ The term of this patent subsequent to _____ has been disclaimed.

☐ The term of this paten not extend beyond the exp of U.S Patent. No. _____

☐ The terminal ____mon this patent have been discl

DRAWINGS			CLAIMS ALLOWED	
Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
35	50	21	22	1

NOTICE OF ALLOWANCE MAILED

Lawrence Parkey (Assistant Examiner) 12/15/05 (Date)

ISSUE FEE

Amount Due	Date Paid

(Primary Examiner) (Date)

ISSUE BATCH NUMBER

(Legal Instruments Examiner) (Date)

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